X-1056 US 10/737,386.

PATENT Conf. No.: 5383

IN THE UNITED STATES PATENT OFFICE

Applicants:

Brian T. Brunn

Assignee:

Xilinx, Inc.

Title:

"Adaptive Transition Density Data Triggered PLL (Phase Locked Loop)"

Serial No.:

10/737,386

File Date:

December 16, 2003

Examiner:

Aslan Ettehadieh

Art Unit:

2611

Docket No.: X-1056 US

Conf. No.:

5383

Mail Stop AMENDMENT

COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, Virginia 22313-1450

AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Dear Examiner:

In response to the First Office Action mailed from the Patent Office on May 14, 2007, please enter the following amendments and entertain the following remarks.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 8 of this paper.